

Team Meeting Recap and Analysis

Date: 1/13/2008

Start time: 11:13am

End time: 11:23am

Total time: 10min

Present: KL, SW, DG, MC

Meeting opens 11:13am

Analog to digital converter is SPI and not I2C.
Another cog may need to be used for SPI communication.
David looked up alternatives, not much available.

2nd order is set up and will be presented to department.

FTA 2nd design review should be scheduled and done soon.
Tentatively, January 27th at 11:00am-12:00pm.

Reminder to everyone, the prototype deliverable will be completed by February 20th. Specifically:

- Phase 1 and Phase 2 should be completed
- Controller and circuitry are on-board
- Functionality includes manual control

SUBSYSTEM UPDATES

Drive Train and Chassis:

Front bumper needs to be worked on in order to provide protection to front end circuitry.

Meeting adjourns 11:22am